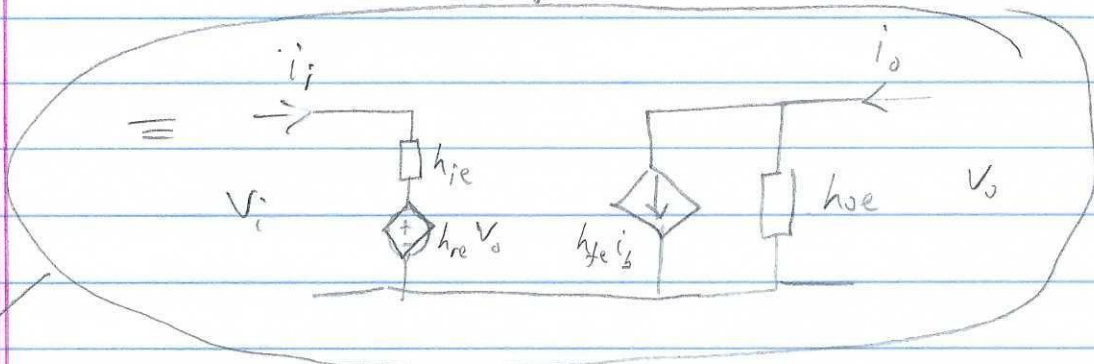
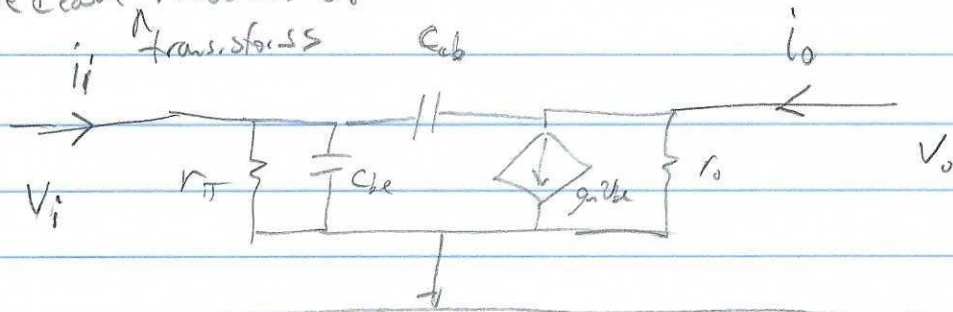


★ Show data sheet for 5179

2-Port Parameters (H, Y, and S)

Recall model used so far



Can also model w/ 2 port parameters (H, Y, S, ...)

H-Parameter Case:

$$\begin{bmatrix} V_1 \\ I_2 \end{bmatrix} = \begin{bmatrix} h_i & h_r \\ h_f & h_o \end{bmatrix} \begin{bmatrix} I_1 \\ V_2 \end{bmatrix}$$

OR:

$$V_i = h_i I_i + h_r V_o$$

$$I_o = h_f I_i + h_o V_o$$

If CE Amp

$$V_i = h_{ie} I_i + h_{re} V_o$$

$$I_o = h_{fe} I_i + h_{oe} V_o$$

NOTE $h_{ie} = \frac{V_i}{I_i} \Big|_{V_o=0} \equiv Z_i \Big|_{V_o=0}$ $h_{re} = \frac{V_i}{V_o} \Big|_{I_i=0} \Rightarrow$ Reverse Gain

Show data sheet & point out how to find $g_m, C_{be}, etc.$ $h_{fe} = \frac{I_o}{I_i} \Big|_{V_o=0} \equiv$ Short-Ckt I gain $h_{oe} = \frac{I_o}{V_o} \Big|_{I_i=0} \equiv Y_o \Big|_{I_i=0}$ Input open

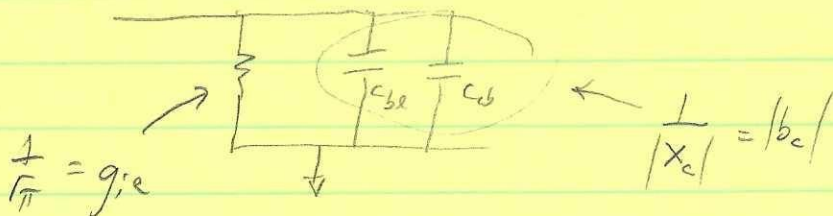
Example 2N5179

Looking at data sheet, for $I_c = 1.5 \text{ mA}$, $V_{ce} = 6 \text{ V}$

$$h_{ie} = g + jb$$

$$= 0.3 \text{ m} + j 4 \text{ m} \Omega \text{ at } 100 \text{ MHz}$$

This corresponds to



$$\Rightarrow r_{\pi} = \frac{1}{.3 \text{ m}} = \underline{3.3 \text{ K}\Omega}$$

$$\Rightarrow X_c = \frac{1}{4 \text{ m}} = 250 \Omega$$

$$\Rightarrow C = \frac{1}{2\pi f X_c} = \underline{6.4 \text{ pF}}$$

Check against formulas: *media guessed*

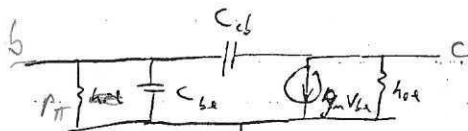
$$r_{\pi} \approx h_{ie} \frac{1}{g_m} \approx 130 \left[\frac{(1.4)(.026)}{1.5 \text{ mA}} \right] = 3.2 \text{ K} \checkmark$$

$$C_{be} \approx \frac{1}{2\pi f_T \frac{1}{g_m}} = \frac{1}{2\pi (320 \text{ MHz})(26 \Omega)} = 8 \text{ pF} \text{ OK}$$

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HF Transistor 2 Port Parameters

Really simplified high freq BJT model used previously ^{h parameters}



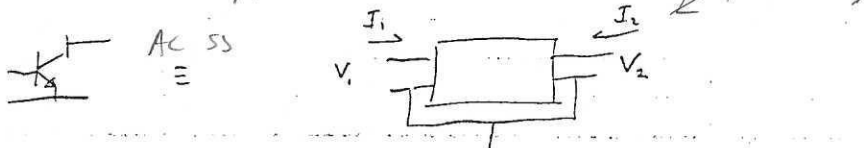
- prob →
- Difficult to measure r_{π} directly due to parasitic L, C in connection
 - At very high frequencies, this model produces poor results and more complex models needed (small signal model)
- Additional problem

- ~~makes circuit analysis complex~~
- Difficult to measure V_{be} at HF due to board/test equipment parasitics
- Difficult to map results to component values

Soln: Use Y param & S-param ^{2-port} models

Y Parameters

Treat Transistor ^{SS AC modeling} as black box



Characterize as

$$\begin{bmatrix} I_1 \\ I_2 \end{bmatrix} = \begin{bmatrix} Y_{11} & Y_{12} \\ Y_{21} & Y_{22} \end{bmatrix} \begin{bmatrix} V_1 \\ V_2 \end{bmatrix}$$

Output

$$I_1 = Y_{11} V_1 + Y_{12} V_2$$

$$I_2 = Y_{21} V_1 + Y_{22} V_2$$

Chd model

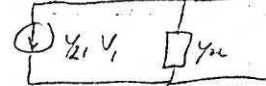
Independent

$$Y_{11} = \frac{I_1}{V_1} \Big|_{V_2=0}$$

$$Y_{12} = \frac{I_1}{V_2} \Big|_{V_1=0}$$

$$Y_{21} = \frac{I_2}{V_1} \Big|_{V_2=0}$$

$$Y_{22} = \frac{I_2}{V_2} \Big|_{V_1=0}$$



Easier to measure because no extra chits required

Easier to work with because simpler model.

No messing with components needed.
Just measure at freqs of interest.

Example: 2N5179 y parameters (Slow Data Sheet)

$y_{ie} \equiv -y_{11}$ Shows behavior of ~~input~~ $\parallel -jX_{c_{be}}$

$y_{oe} \equiv y_{22}$ Shows behavior of $\parallel h_{oe} \parallel -jX_{c_{ce}}$

$y_{fe} \equiv y_{21}$ Shows behavior of $g_m \beta \parallel -jX_{c_{cb}}$

$y_{re} \equiv y_{12}$ Shows behavior of $-jX_{c_{cb}}$



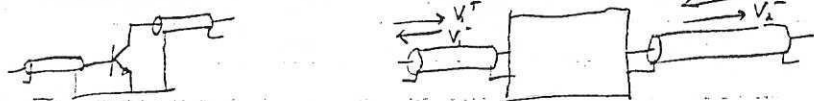
Soln: S Parameters

Connect on CE vs CB. E. Show transformation

Problem: Difficult to measure $\geq 10\text{Hz}$ or above [Hard to create short chits
Stability problems]

~~Fixed transistor on black box~~

Use 50 Ω TX lines for connections. Use 50 Ω SRE load



Describe as

$$\begin{bmatrix} V_1^- \\ V_2^- \end{bmatrix} = \begin{bmatrix} S_{11} & S_{12} \\ S_{21} & S_{22} \end{bmatrix} \begin{bmatrix} V_1^+ \\ V_2^+ \end{bmatrix}$$

$$V_1^- = S_{11} V_1^+ + S_{12} V_2^+$$

$$V_2^- = S_{21} V_1^+ + S_{22} V_2^+$$

$S_{11} = \left. \frac{V_1^-}{V_1^+} \right|_{V_2^+ = 0}$ Input refl coeff w/ output terminated in 50 Ω (\sim input Γ)

$S_{22} =$ Output " (\sim output Γ)

$S_{21} =$ Forward trans coeff (V_{out} is / 50 Ω I/V)

$S_{12} =$ Reverse trans coeff (Reverse isolation in 50 Ω ports)

(Slow Data Sheet) Connect on CE vs CB